

Form PTO-1449 (REV. 8-83)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 42390.P4918	SERIAL NO. 09/053,006
	APPLICANT Mohammad Abdallah, et al.		
	FILING DATE 3/31/98	GROUP 2787	
INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>			

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (*Including Author, Title, Date, Pertinent Pages, Etc.*)

<i>LWD</i>	<i>LWD</i>	Sun Microsystems, "Visual Instruction Set User's Guide", March, 1997.
<i>LWD</i>	<i>LWD</i>	Tri Media, "TM 1000 Preliminary Data Book", Phillips Electronics North America Corporation, 1997.
<i>LWD</i>	<i>LWD</i>	Wang, Mangaser, Srinivasan, "A Processor Architecture for 3D Graphics Calculations", Computer Motion Inc., Goleta, CA.

EXAMINER <u>Lance W. Sealey</u>	DATE CONSIDERED <u>1/12/08</u>
------------------------------------	-----------------------------------

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form PTO-1449 (REV. 8-83)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 42390.P4918	SERIAL NO. 09/053,006
APR 21 1999			
INFORMATION DISCLOSURE CITATION			
(Use several sheets if necessary)			

U.S. PATENT DOCUMENTS

RECEIVED
2-1989

APR 22 1999

99 JUN 10 PM 1:28

RECEIVED

Group 2700

GRO
TON.
No
2700

FOREIGN PATENT DOCUMENTS

OTHER DOCUMENTS (*Including Author, Title, Date, Pertinent Pages, Etc.*)

<i>LWA</i>	<i>LWA</i>	Abbott, Massalin, Peterson, Karzes, Yamano, Kellogg, "Broadband Algorithms with the MicroUnity Mediaprocessor", Proceedings of Compcon, IEEE, 1996, pp. 349-354.
	<i>LWA</i>	Advanced Micro Devices, Inc., "AMD-3D Technology Manual", February, 1998.
<i>LWA</i>	<i>LWA</i>	Hansen, Craig, "Architecture of a Broadband Mediaprocessor", Proceedings of Compcon, IEEE, 1996, pp. 334-340.
<i>LWA</i>	<i>LWA</i>	Hayes, Loyola, Abbott, Massalin, "MicroUnity Software Development Environment", Proceedings of Compcon, IEEE, 1996, pp. 341-348.
<i>LWA</i>	<i>LWA</i>	Levinthal, Hanrahan, Paquette, Lawson, "Parallel Computers for Graphics Applications", Proceedings of ASPLOS II, IEEE, 1987, pp. 193-198.
<i>LWA</i>	<i>LWA</i>	Levinthal and Porter, "Chap-A SIMD Graphics Processor", Computer Graphics Project, ACM, Vol. 18, No. .3, July 1984, pp. 77-81."

EXAMINER

Lance W. Seale

DATE CONSIDERED

1/12/08

***EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.